Preface

The International Workshop on FPGAs for Software Programmers (FSP) now enters its third year. As the workshop has traditionally been held co-located with the International Conference on Field-Programmable Logic and Applications (FPL), it has moved to Lausanne for 2016. For the first time, FSP publishes its results in IEEE Xplore-indexed proceedings, marking a major increase in visibility over prior events. This step was enabled by the kind sponsorship of PLDA.

However, the key focus of FSP remains on looking at key aspects of using FPGA-based reconfigurable computing from a software developer's perspective. Contributions include not only the presentation of solutions, but also studies on the precise nature of the difficulties to guide further research. Furthermore, FSP is keen to maintain its workshop nature: It remains a venue for presenting not just mature research, but also exposing promising work-in-progress or prototype reports to a wider community for inspiring scientific discourse.

In this spirit, based on recommendations by the Program Committee, the FSP Chairs selected from the 11 submissions (coming from seven countries) six to be presented as full-papers, and one as a short paper. These cover a wide range of topics, ranging from tools and design methodology to a fundamental examination of the lack of a middleware ecosystem for reconfigurable computing. The Chairs would like to thank the Program Committee for their timely reviews, enabling us to aim for three reviews to base our decisions on.

Presented at the workshop itself were two keynotes (one from industry, one from academia), as well as an embedded tutorial on the Pynq open-source hardware design framework.

We hope you find these presentations interesting and look forward to receiving your own submission to future FSP events!

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